

**Vita**  
**Gordon Morrison**

- Education in Applied Mathematics and Computer Science - with Compiler Emphasis / Computer Languages / Computer Architecture / Model Driven Development / Other Advanced Topics and Patent Law
  - **Outstanding** Alumni Nomination (by Dr. Joseph Rabb); **Who's Who** in the West, 20<sup>TH</sup> Edition
- Hired as an **Honors** Graduate, NOAA - Wave Propagation Laboratories, (Real-time Weather Radar)
- Special Achievement **Award** for Implementation of Real-time Radar Control, DSP, and Graphics
- **FCC License** (General Radiotelephone Certificate, PG-15-14226); **SEC License** (Series 7 and Series 63, CRD# 1968943); IEEE Senior Member; Completed US Coast Guard Training for Merchant Marine Captain's License
- **AUTHOR:** *Breaking the Time Barrier – The Temporal Engineering of Software* published Feb 2009 available at [www.amazon.com](http://www.amazon.com) ; Book Reviews at: [www.vsmertlot.com](http://www.vsmertlot.com); **Webinar:** Reducing Software Complexity; **Paper:** “**Understanding Temporal Logic**” January 2010 issue devoted to Model-Driven Development of Software Tech News ([www.softwarettechnews.com](http://www.softwarettechnews.com)). **Subject Matter Expert** for: the Software Development Tools and Technology Information Clearinghouse at ([www.sdtatic.com](http://www.sdtatic.com)).
- **SPEAKER:** April 2010-Systems & Software Technology Conference ([www.sstc-online.org](http://www.sstc-online.org)); Webinar The DACS; IEEE Computer Society on temporal engineering. Lockheed-Martin Advanced Technology Labs; Stanford Research International on software architecture; U of Missouri on patent litigation as inventor; U of Houston on temporal engineering;
- **INVENTION:** Coherent Object System Architecture (**COSA**)
  - COSA is architected to reduce the complexities of the way software is used. COSA allows the user to produce specifications – requirements, process model, data model, object model, project management, application management, AND the manufacture of executable code in the same **simple** paradigm. The COSA approach can replace over eighty different applications. COSA eliminates spaghetti code and provides exceptional trace capabilities that reduce the costs of debugging.
  - **#6,345,387** Patent Issued February 5<sup>th</sup> 2002, **Cited in 8 other Patents by IBM, NEC, Health Language, Fujitsu, Xilinx, Coimbra, and Sun Microsystems! Technology used by IAR Systems ([www.iar.com](http://www.iar.com)).**
- **LITIGATION EXPERIENCE**
  - With over 400 hours of deposition experience defending my TDA patents against IBM, Motorola, Apple Computer, Texas Instruments, Phillips AG, Intel, and others. Over 3,000 pages of deposition testimony.
  - Testified as a fact witness before the International Trade Commission in patent litigation.
  - Consulted to HP and Intel in their litigation with Intergraph. I proved Intergraph's patent to be inoperable.
- **INVENTION:** Technology similar to Java platform
  - Implemented a platform & machine independent serial and parallel virtual machine, February 1990
  - Registered with the Patent and Trademark Office (1990)
  - Fully Parallel Implementation of Virtual Machine was Demonstrated in July 1990
  - Venture Capital Meeting with the Mayfield Fund in Menlo Park California (30 Nov 90)
    - Technology Reviewed (and initialed) by Dr John Hennessey, Dean of Engineering Stanford University
    - Present for the Mayfield Meeting at 2200 Sandhill Road
      - Mike Leventhal, John Hennessey, Vinod Khosla, and others
      - No perceived market for open solutions and platform independence.
  - Invented and Built a Twin Recumbent Bicycle, Riders sit Side-by-Side, 21 independent speeds, 68 pounds
- **INVENTION: TDA** (Time Domain Architecture) Multi-Core, Hyper-Threading, Multi-Context, Super-Scalar Technology
  - Industry Pioneering Patents for Hardware and Software Architectures
  - Lookup patent numbers at [www.uspto.gov/patft/index.html](http://www.uspto.gov/patft/index.html).
    - #4,847,755 Patent Issued 1989, **Cited in 168 other Patents! (EPIC, Multi-Core and Hyper-Threading)**
    - #5,021,945 Patent Issued 1991, **Cited in 101 other Patents! (Explicitly Parallel Instruction Software)**
    - #5,517,628 Patent Issued 1996, **Cited in 50 other Patents! (Multiple Register Files, Parallel States)**
    - #5,765,037 Patent Issued 1998, **Cited in 29 other Patents! (Multiple Instruction Delayed Branch)**
    - #6,253,313 Patent Issued 2001, **Cited in 24 other Patents! (Multiple Condition Codes)**
  - **372 total Citings! The most highly cited computer architecture patent.**
  - **Licensed by IBM, Apple, Motorola, TI, Kenwood, iBiquity Digital Radio, Intel, Analog Devices, Fijitsu, Sun Microsystems, and others.**